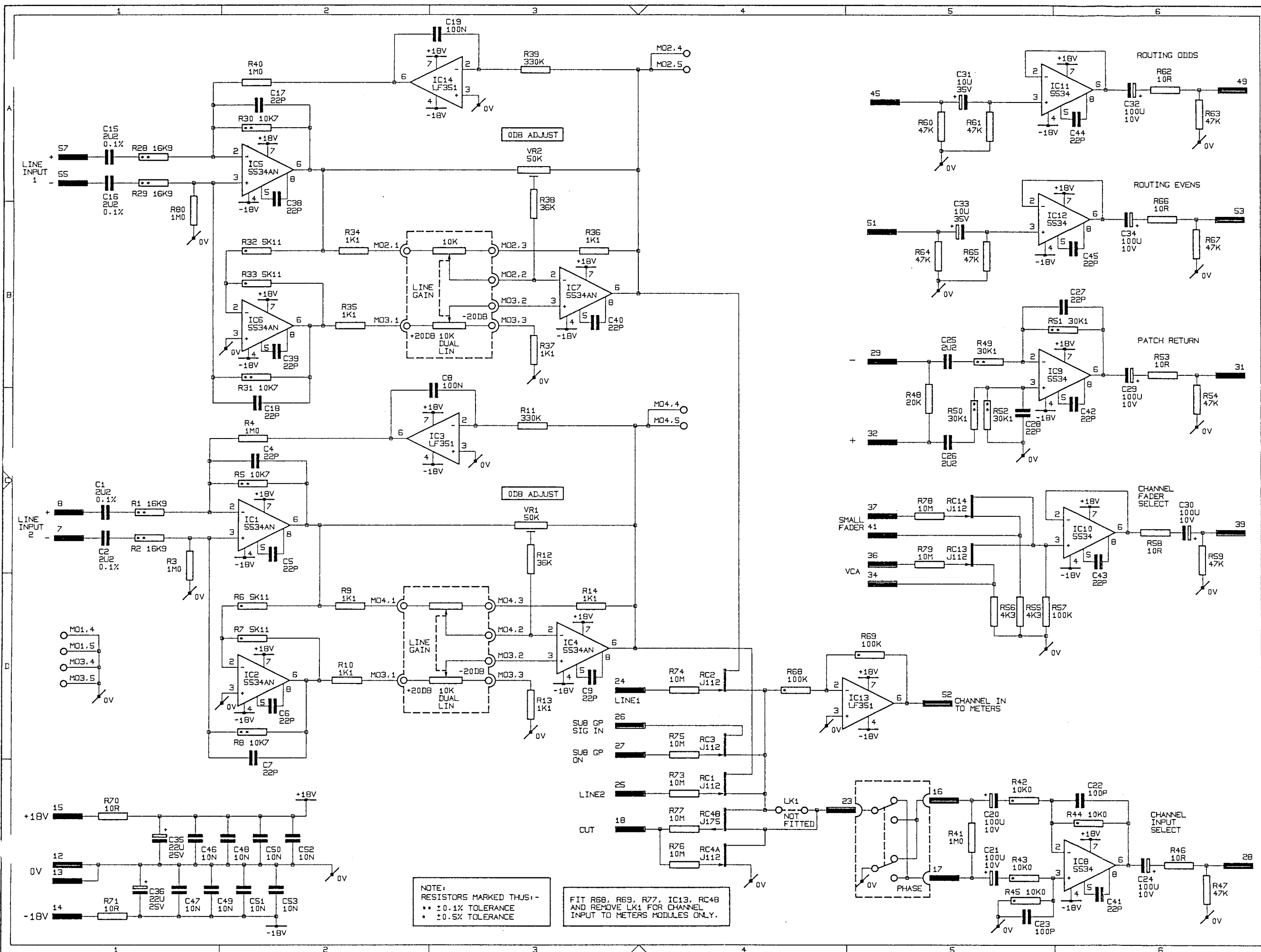


82E351 Channel Amplifier Card Edge Connector

From: 82E351

To: 611G/V/E Mother Board

Pin	Function	Pin	Function
1	NP	32	Insert Return +
2	NP	33	NP
3	NP	34	RC13 Input from VCA O/P (82E13 p24)
4	NP	35	NP
5	NP	36	RC13 Control. VCA to Routing
6	NP	37	RC14 Control. SF to Routing
7	Line 2 Input -	38	NP
8	Line 2 Input +	39	IC10 Output to Routing Pan Pot
9	NP	40	Keyway
10	NP	41	RC14 Input from SF O/P (82E294 p47)
11	NP	42	NP
12	0V	43	NP
13	0V	44	NP
14	-18V	45	IC11 Input. Odd Routing Buffer
15	+18V	46	NP
16	IC8 Input - from Phase Switch	47	NP
17	IC8 Input + from Phase Switch	48	NP
18	RC4 Control. Input Mute	49	IC11 Output to Odd Routing Switches
19	NP	50	NP
20	NP	51	IC12 Input. Even Routing Buffer
21	NP	52	Channel into Meters Send
22	NP	53	IC12 Output to Even Routing Switches
23	Ch. Input to Phase Reverse Switch	54	NP
24	RC2 Control. Line 1 Input Select	55	Line 1 Input -
25	RC1 Control. Line 2 Input Select	56	NP
26	RC3 Signal. Subgroup I/P	57	Line 1 Input +
27	RC3 Control. Subgroup I/P Select	58	NP
28	IC8 Output to Split Switch	59	NP
29	Insert Return -	60	NP
30	NP	61	NP
31	Output to EQ Switching (82E312)	62	NP



COPYRIGHT © SSI. ALL RIGHTS RESERVED. THIS DOCUMENT MAY NOT BE REPRODUCED IN ANY FORM WITHOUT THE WRITTEN PERMISSION OF SOLID STATE LOGIC.				
REV	ISSUE	DATE	DETAILS	
0	A	7 AUG 90	NEW DRAWING	RP
0	B	12 OCT 90	R72 C37 REMOVED BC NAMES ADDED	MM
1	A	30 JAN 91	ECO 4K/701 R68 R69 WERE 5% NOW 0.5%	BC MM

SHEET 1 OF 1		A2
TITLE		611E/V/G DUAL LINE INPUT AMP
DRG. NO.		T82351.71

Solid State Logic